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**RESPONSE UNDER 37 C.F.R. § 1.116
EXPEDITED PROCEDURE - EXAMINING GROUP 2810**

PATENT

(12)
#15C
9/14/03

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants : Walter de Coster et al.
Application No. : 09/823,274
Filed : March 29, 2001
For : MOS TRANSISTOR IN AN INTEGRATED CIRCUIT AND
ACTIVE AREA FORMING METHOD

ok
entry
9/29/03

Examiner : Paul E. Brock II
Art Unit : 2815
Docket No. : 859063.491
Date : September 11, 2003

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RESPONSE/AMENDMENT UNDER 37 C.F.R. § 1.116

Commissioner for Patents:

In response to the Office Action dated June 11, 2003, please amend the application as follows and please reconsider the application in view of the discussion in the Remarks section below.

Amendments to the Claims are reflected in the listing of claims which begins on page 2 of this paper.

Amendments to the Drawings begin on page 7 of this paper and include an attached Replacement Sheet.

Remarks/Arguments begin on page 8 of this paper.

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Approved for use through 10/31/2002. OMB 0651-0031

U.S. Patent and Trademark Office: U.S. DEPARTMENT OF COMMERCE

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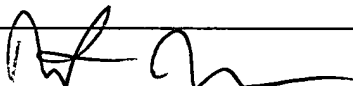
Application Number	09/823,274
Filing Date	March 29, 2001
First Named Inventor	Walter de Coster
Group Art Unit	2815
Examiner Name	Paul E. Brock, II
Attorney Docket No.	859063.491

ENCLOSURES (check all that apply)

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<input checked="" type="checkbox"/> After Final	<input type="checkbox"/> Petition	<input type="checkbox"/> Appeal Communication to Group (<i>Appeal Notice, Brief, Reply Brief</i>)
<input type="checkbox"/> Affidavits/declaration(s)	<input type="checkbox"/> Petition to Convert to a Provisional Application	<input type="checkbox"/> Proprietary Information
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Remarks 1 Sheet of Drawings – Fig. 1-3.

SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT

Individual Name	Robert Iannucci – Reg. No. 33,514	Customer Number 00500
Signature		
Date	September 11, 2003	

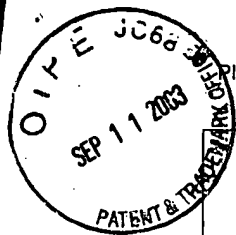
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